

### REMARKS

The Examiner pointed out several typographic errors in the text. Applicant has amended the appropriate parts of the application to address these issues.

The Examiner has rejected claims 9-21 and 23-36 under 35 U.S.C. Section 112, first paragraph. According to the Examiner, how the layers of Fig. 1 and 2 fit into the device of Fig. 4 is not clearly described to enable any person skilled in the art to make and use the invention. In addition, the Examiner has requested clarification as to the layers of Figs. 1 and 2 through which charge reaches the output in Fig. 4. Applicant respectfully traverses the Examiner's rejection and maintains that the specification of the present application along with knowledge well known in the art enables one skilled in the art to make and use the quantum-well based solid-state imaging devices as described therein and thus satisfies the requirements of 35 U.S.C. §112, first paragraph.

Applicant has provided herewith a declaration by the Inventor, Geoff W. Taylor, that sets forth the basis for the patentability of the claimed invention.

In light of all of the above, it is submitted that the claims are in order for allowance, and prompt allowance is earnestly requested. Should any issues remain outstanding, the Examiner is invited to call the undersigned attorney of record so that the case may proceed expeditiously to allowance.

Respectfully submitted,



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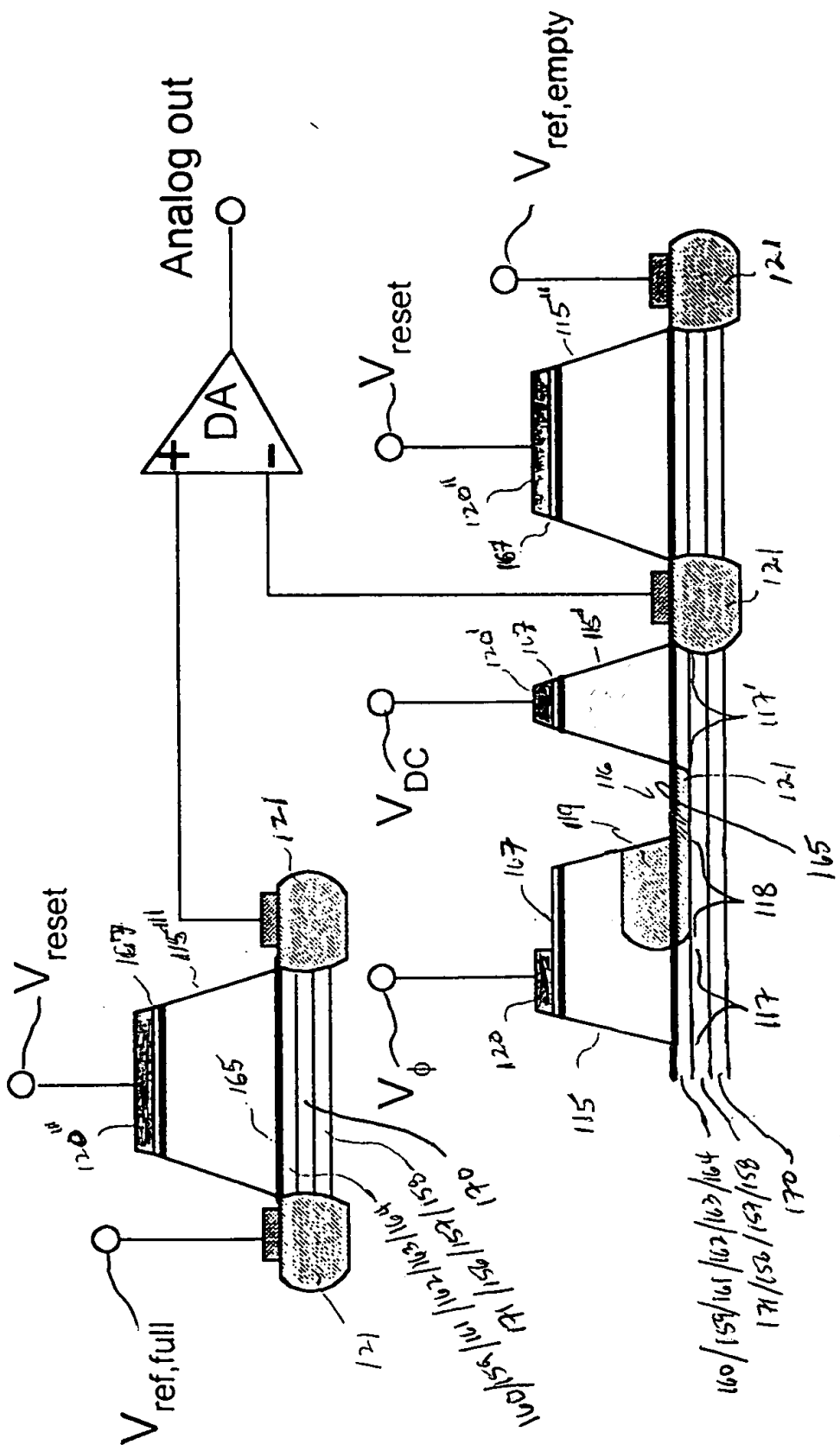


FIG.4